

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

#5/a
10/15/00
Surb

In re Application of:)
Suzawa et al.)
Serial No.:)
Filed: Herewith)
For: Wiring And Manufacturing Method Thereof,)
Semiconductor Device Comprising Said)
Wiring, And Dry Etching Method)
Examiner:)
Art Unit:)

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Date of Deposit July 13, 2000

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Name Tricia Senese
(typed or printed)

Signature Tricia Senese

Assistant Commissioner for Patents
Washington D.C. 20231

July 5, 2000

PRELIMINARY AMENDMENT A

Prior to calculation of fees and examination, please enter the following amendment in the above-identified application:

IN THE CLAIMS:

Please amend the claims as follows:

- Claim 3, ln. 1, delete "or 2".
- Claim 4, ln. 1, delete "or 2".
- Claim 8, ln. 1, delete "or 7".
- Claim 9, ln. 1, delete "or 7".
- Claim 10, ln. 1, delete "or 7".
- Claim 11, ln. 1, delete "or 7".
- Claim 18, ln. 1, delete "or 17".
- Claim 19, ln. 1, delete "or 17".
- Claim 27, ln. 1, delete "or 26".